

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Shunpei YAMAZAKI et al

Serial No.: Not Assigned

Filed: Herewith

Art Unit:

For: SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF

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) Name: Yue & Ruan
) (typed or printed)

) Signature Yue & Ruan

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. A copy of each of the references was submitted to the Patent Office or was cited by the Examiner in the parent application, Serial No. 09/619,732, filed July 19, 2000, which is being relied upon for an earlier filing date under 35 U.S.C. 120 (37 C.F.R. §1.98(d)).


No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

A first office action, notice of allowance or issue fee notification has not been received in this case, so applicant does not believe that a fee is due. However, if any such fee is required, please charge our Deposit Account No. 50/1039.

Respectfully submitted,


Mark J. Murphy
Registration No: 34,225

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LIST OF PUBLICATIONS CITED BY APPLICANT			Atty. Docket No. 0553-0200.01		Serial No. Not Assigned	
			Applicant Shunpei YAMAZAKI et al			
			Filing Date Herewith		Group	
U.S. PATENT DOCUMENTS						
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
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	5,643,826	07/01/97	Ohtani et al	437	88	10/25/94
	5,686,328	11/11/97	Zhang et al	437	41	07/11/94
	5,767,547	06/16/98	Merchant et al	257	347	05/23/95
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	6,335,290	01/01/02	Ishida	438	705	03/29/99
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	JP 4-369271	12/22/92	Casio Computer Co.		X	06/17/91
	JP 5-102483	04/23/93	Sharp Corp.		X	10/09/91
	JP 6-148685	05/27/94	Toshiba Corp.	X	X	11/13/92
	JP 7-130652	05/19/95	Semiconductor Energy Lab Co. Ltd.	X		10/29/93
	JP 7-235680	09/05/95	Toshiba Corp.	X	X	02/24/94
	JP 8-274336	10/18/96	Toshiba Corp.	X	X	03/30/95
	JP 9-293600	11/11/97	Matsushita Electric Ind. Co. Ltd.	X		01/08/97
OTHER PRIOR ART - NON-PATENT LITERATURE DOCUMENTS (Including Author, Title, Date, Pertinent Pages)						
EXAMINER:				DATE CONSIDERED:		

LIST OF PUBLICATIONS CITED BY APPLICANT			<u>Attorney Docket No.</u> 0553-0200.01	<u>Serial No.</u> Not Assigned		
			<u>Applicant</u> Yamazaki et al			
			<u>Filing Date</u> Herewith	<u>Group</u>		
U.S. PATENT DOCUMENTS						
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	4,394,182	07/19/83	Maddox, III			10/14/81
	5,247,190	09/21/93	Friend et al.			04/18/90
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	5,528,397	06/18/96	Zavracky et al.			07/28/94
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	JP 09-191111	07/22/97	Zhang			07/26/96
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	JP 10-294280	11/04/98	Yamazaki et al.			02/09/98
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	WO 90-13148	11/01/90	Friend et al.			04/18/90

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- 1) Schenk et al., "Polymers For Light Emitting Diodes," EuroDisplay '99 Proceedings, pp. 33-37 (September 1999).
- 2) Hatano et al., "A Novel Self-aligned Gate-overlapped LDD Poly-Si TFT With High Reliability And Performance," IEDM Technical Digest, pp. 523-526 (December 1997).
- 3) Shimokawa et al., "Characterization Of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal Of Applied Physics, Vol. 27, No. 5, May, 1988, pp. 751-758
- 4) JP 08-78329 English abstract.
- 5) JP 09-191111 English abstract.
- 6) JP 10-92576 English abstract.
- 7) JP 10-104659 English abstract.
- 8) JP 10-135468 English abstract.
- 9) JP 10-135469 English abstract.
- 10) JP 10-233511 English abstract.
- 11) JP 10-294280 English abstract.
- 12) JP 11-191628 English abstract.
- 13) JP 11-345767 English abstract.
- 14) JP 11-354442 English abstract.
- 15) US Patent Application No. 09/433,705 (pending) including specification, claims, abstract and drawings.
- 16) US Patent Application No. 09/618,930 (pending) to Yamazaki et al., including specification, claims, abstract, drawings and PTO filing receipt.
- 17) US Patent Application No. 09/714,891 (pending) including specification, claims, abstract and drawings.

EXAMINER:

DATE CONSIDERED:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.